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Representation by IIT

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module input adder(A, B, cout, sum); [3:0] A, B; adder A B output cout; output [3:0] sum;
// HDL modeling of // adder functionality
cout sum endmodule
Don't forget the semicolon!

always @(posedge clk) begin - MIT

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DFFs shifts data to the right only when the control signal is high.

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