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Compiler—The compiler translates your OpenCL C device code into a hardware configuration file that can be loaded onto an Altera FPGA. Altera SDK for OpenCL (AOCL) Utility —The AOCL utility includes a set of

Altera SDK for OpenCL Programming Guide - intel.cn

set up and use the Altera® Software Development Kit (SDK) for OpenCL™ (AOCL) to run an OpenCL application on the Altera Cyclone® V SoC Development Kit. OpenCL (1) is a C-based open standard for the

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Intel® SDK for OpenCL™

Applications

The Intel FPGA SDK for OpenCL provides you an environment to target FPGAs while abstracting FPGA details. (1) OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission of the Khronos Group™. (2) The Intel FPGA SDK for OpenCL is based on a published Khronos specification, and has passed the Khronos Conformance Testing Process.

Intel FPGA SDK for OpenCL

The Altera SDK for OpenCL enables programmers to leverage the massively

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Electro-Magnetic World: Altera SDK for OpenCL

1. Download the .tar file. 2. Extract the files into temporary directory. 3. Run the setup.sh file to install Intel FPGA SDK for OpenCL with Quartus Prime Standard Edition. 4. Run the setup_pro.sh file to install Intel FPGA SDK for OpenCL with Quartus Prime Pro Edition.

Download Center for FPGAs

Optimization Training OpenCL™ Coding Optimizations for Intel® Stratix® 10 Devices (23 minutes). In this course, we will cover how the offline kernel compiler of the Intel® FPGA SDK for OpenCL™ optimizes OpenCL kernel code for optimal performance on Intel® Stratix® 10 FPGAs and how to use recommended coding constructs to enable these optimizations.

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The SDK has now been made public. Altera's implementation is built on top of OpenCL 1.0 but offers custom extensions to tap into the unique features of FPGAs. More information can be found on...

A Look at Altera's OpenCL SDK for FPGAs - AnandTech

Querying platform info on my host code I get: OpenCL 1.0 Altera SDK for OpenCL, Version 15.0 . I have a few questions and problems that I would be extremely grateful if someone can help me with. 1) Using the emulator, I timed my kernels for the FPGA.

SLAM on FPGA using Altera OpenCL - Intel Community

SAN JOSE, Calif., June 30, 2014 /PRNewswire/ -- Altera Corporation today dramatically simplified a programmer's ability to accelerate algorithms in FPGAs. The Altera SDK for OpenCL version 14.0... | October 15, 2020