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Virtex-4 FPGA PCB Designer's Guide www.xilinx.com UG072 (1.2) June 24, 2008 Xilinx is disclosing this user guide, manual, release note, and/ or specification (the "Documentation") to you solely for use in the development of designs to operate with Xilinx hardware devices.

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DDR2/DDR3 Low-Cost PCB Design Guidelines for Artix-7 ...

What are the differences between different PCB revisions of the Kintex-7 FPGA KC705 Evaluation Kit? Solution (Xilinx Answer 59750) Kintex-7 FPGA KC705 Evaluation Kit - Changes from rev 1.0 to rev 1.1 (Xilinx Answer 59751) Kintex-7 FPGA KC705 Evaluation Kit - Changes from rev 1.1 to rev 1.2

AR# 59749: Kintex-7 FPGA KC705 Evaluation Kit - PCB ...

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Xilinx Artix-7 - PCB Design Software & Tools | Altium

What are the differences between different PCB revisions of the Virtex-7 FPGA VC707 Evaluation Kit? Solution See (Xilinx Answer 59753) Virtex-7 FPGA VC707 Evaluation Kit - Changes from rev 1.0 to rev 1.1

AR# 59752: Virtex-7 FPGA VC707 Evaluation Kit - PCB ...

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Spartan Edge Accelerator Board - Arduino FPGA Shield with ...

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PMP7977 Xilinx Artix 7 FPGA with PMBus Power Management ...

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