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FPGA DE1-SoC Cyclone V Overlay Device Tree by CARAM on 11-13-2020 04:20 AM Latest post on 11-25-2020 10:36 AM by Eliath\_G\_Intel 1 Reply 84 Views

### **FPGA, SoC, And CPLD Boards And Kits - Page 4 - Intel Community**



The Intel SoC FPGA Embedded Development Suite Standard Edition, Version 20.1 includes functional and security updates. Users should keep their software up-to-date and follow the technical recommendations to help improve security. Additional security updates are planned and will be provided as they become available.

### **Download Center for FPGAs**

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The Intel FPGA SDK for OpenCL Software Pro Edition, Version 19.4 is subject to removal from the web when

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## **GX/SX Device Overview**

If your design uses VHDL, refer to this KDB solution to avoid an issue that might cause a hardware failure if you synthesize in Quartus Prime Pro Edition Design Software, Version 20.3. The Quartus Prime Pro Edition Design Software, Version 20.3 includes functional and security updates. Users should keep their software up-to-date and follow the technical recommendations to help improve security.

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Starting with Quartus v20.1 Intel® ships Arm DS for Intel® SoC FPGAs. Arm DS-5 can be used with Quartus v19.4 and earlier versions. Please refer

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TI USB GUI to access LMK04828 on Arria 10 SoC Development kit by SoC\_Developer on 10-11-2020 05:25 AM Latest post on 10-11-2020 11:02 PM by Deshi\_Intel 1 Reply 75 Views

## **FPGA, SoC, And CPLD Boards And Kits - Page 8 - Intel Community**

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