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An Introduction to GPU Programming with CUDA

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Sanders & Kandrot, CUDA by Example: An Introduction to ...

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CUDA by Example: An Introduction to General-Purpose GPU ...

CUDA by Example: An Introduction to General-Purpose GPU Programming. Jason Sanders, Edward Kandrot. "This book is required reading for anyone working with accelerator-based computing systems." -From the Foreword by Jack Dongarra, University of Tennessee and Oak Ridge National Laboratory CUDA is a computing architecture designed to facilitate the development of parallel programs.

CUDA by Example: An Introduction to General-Purpose GPU ...

Since hist_gpu_gmem_atomics.cu requires compute capability 1.1 to function properly, the easiest way to compile this example is, > nvcc -arch=sm_11 hist_gpu_gmem_atomics.cu Similarly, hist_gpu_shmem_atomics.cu relies on features of compute capability 1.2, so it can be compiled as follows: > nvcc -arch=sm_12 hist_gpu_shmem_atomics.cu Compiling Examples with OpenGL and GLUT Dependencies ----- The following examples use OpenGL and GLUT (GL Utility Toolkit) in order to display their results ...

GitHub - chris-ricketts/cuda-by-example

CUDA by Example addresses the heart of the software development challenge by leveraging one of the most innovative and powerful solutions to the problem of programming the massively parallel accelerators in recent years. This book introduces you to programming in CUDA C by providing examples and

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As of December 2015 the official page for CUDA by Example has moved to https://developer.nvidia.com/cuda-example from where you can download the sample chapter and example code including book.h and cpu_bitmap.h.

CUDA By Example utility libraries - Stack Overflow

torch.backends.cuda.cufft_plan_cache.clear() clears the cache. To control and query plan caches of a non-default device, you can index the torch.backends.cuda.cufft_plan_cache object with either a torch.device object or a device index, and access one of the above attributes.

CUDA semantics — PyTorch 1.7.0 documentation

The CUDA C compiler, nvcc, is part of the NVIDIA CUDA Toolkit.

To compile our SAXPY example, we save the code in a file with a .cu extension, say saxpy.cu. We can then compile it with nvcc. nvcc -o saxpy saxpy.cu. We can then run the code: % ./saxpy Max error: 0.000000 Summary and Conclusions

An Easy Introduction to CUDA C and C++ - NVIDIA Developer

CUDA C++ Programming Guide PG-02829-001_v11.1 | ii Changes from Version 11.0 ▶ Added documentation for Compute Capability 8.x. ▶ Updated section Arithmetic Instructions for compute capability 8.6. ▶ Updated section Features and Technical Specifications for compute capability 8.6.

CUDA C++ Programming Guide - Nvidia

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scan - CUDA Parallel Prefix Sum (Scan) This example demonstrates an efficient CUDA implementation of parallel prefix

sum, also known as "scan". Given an array of numbers, scan computes a new array in which each element is the sum of all the elements before it in the input array.

CUDA Samples :: CUDA Toolkit Documentation

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